



1. Features

- (1) 3.3V~5V supply voltage
- (2) Low power consumption
- (3) High speed: 15MBC(typical)
- (4) $V_{CM}=1000V$, and the lowest common mode inhibition, (CMR) is $>120dB$
- (5) $-40^{\circ}C \sim +110^{\circ}C$ temperature of AC and DC performance.



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(7) in compliance with RoHS, REACH standards

output is driven by an internal common-emitter driver stage and a high-speed optical detector. This design provides good ac and dc isolation between the input and output ends of the photoelectric coupler. The characteristic of the photodetector is a collector open circuit schottky clamp transistor. The total input capacitance is typically 5nF at $V_{CM}=0V$ and $f=100kHz$. The photodetector response time range ranges from 40ns to 400ns.

- 2. A/D, D/A converted digital signal isolation
- 3. eliminate noise from the ground loop
- 4. switching power supply
- 5. interface of microprocessor system, computer and peripheral equipment

2. Functional Diagram



1	2	3	4	5	6
NC	NC	NC	NC	NC	NC

5. Absolute Maximum Ratings ($T_a=25^{\circ}\text{C}$)

	Parameter	Symbol	Rated Value	Unit
Input	Average Forward Input Current	I_F	20	mA
	Reverse Input Voltage	V_R	5	V
	Power Dissipation	P_i	40	mW
	Enable Input Voltage	V_E	VCC+0.5	V
	Enable Input current	I_E	5	mA
Output	Output Collector Current	I_O	50	mA
	Output Collector Voltage	V_O	7	V
	Output Collector Power Dissipation	P_O	85	mW

